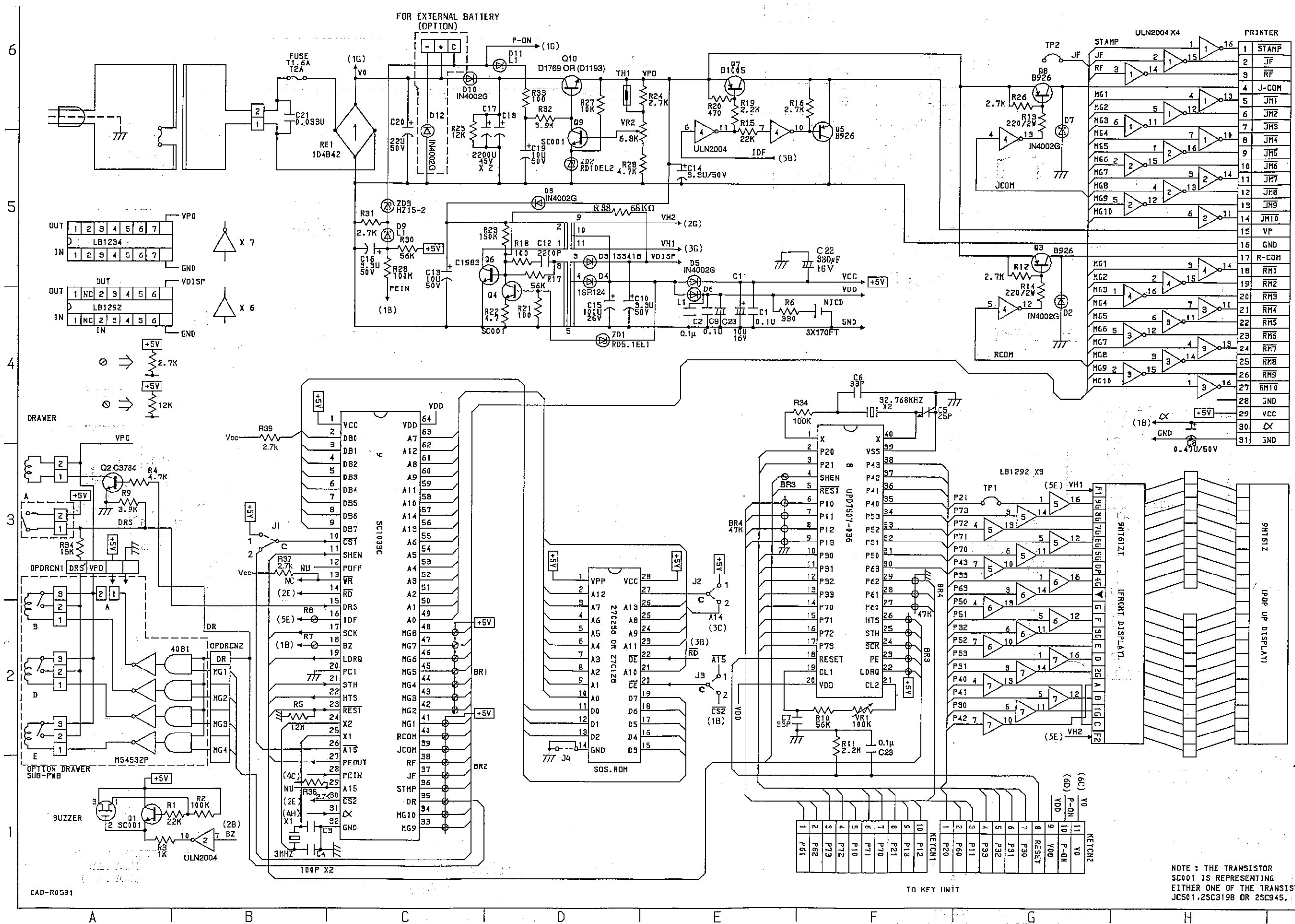


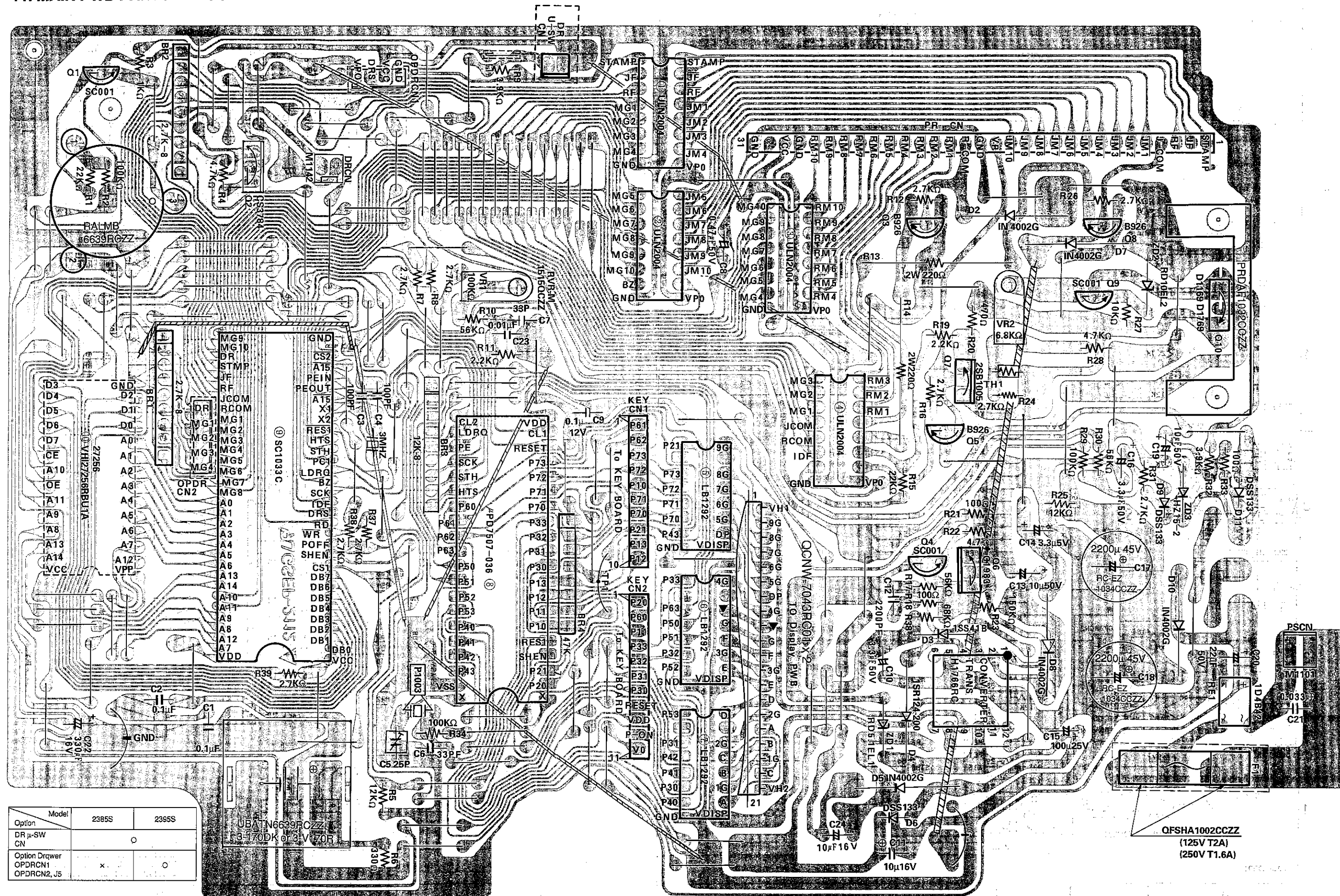
# 10. MAIN CIRCUIT DIAGRAM

- CAVAT
- 2) test No.2 for the sum check data.
- operation, the test nos 1 thru 5.
- is equipped for test NO.5, the next step when the drawer is opened. If the sensor is not jumps to a next step assuming stalled.
- for the continuous print test of
- oked, if the 27128 SOS ROM is 6 SOS ROM is mounted at test
- Clerk and mode switch test
- Key switch test
- Display test
- Receipt switch test
- Drawer test
- Print test
- ROM sum check
- RAM sum check
- Stamping
- sum of key position codes y switch test.
- on finishing the stamp test.
- CAVAT
- est No.2 for the sum check data.
- pt that ignores the drawer



NOTE : THE TRANSISTOR SC001 IS REPRESENTING EITHER ONE OF THE TRANSISTORS JC501,2SC3198 OR 2SC945.

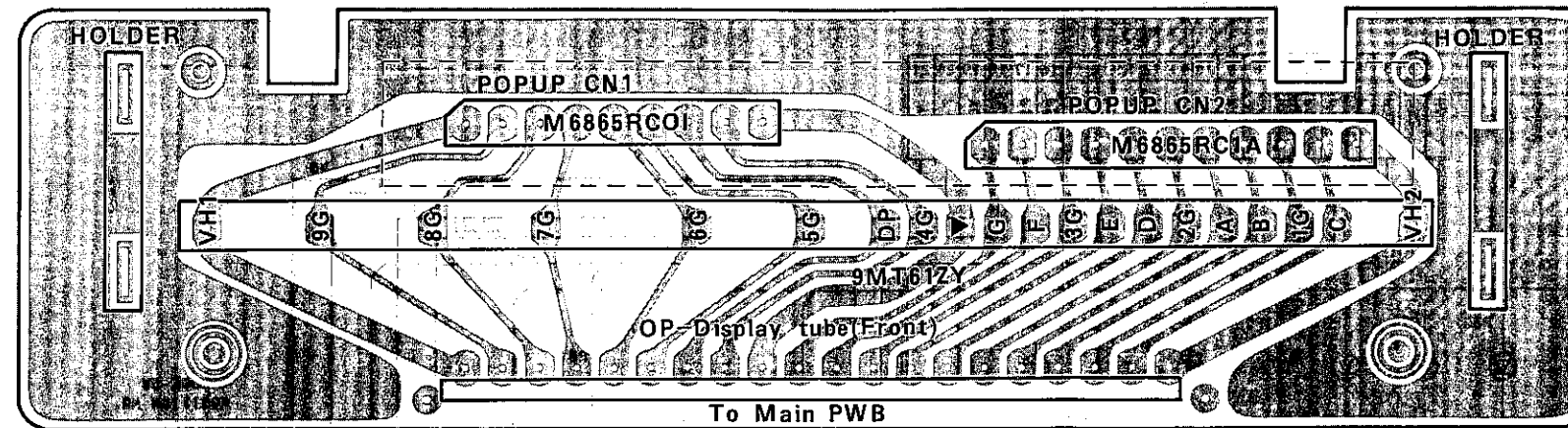
## 11. MAIN PWB PARTS LAYOUT



| Model                                   | 2385S | 2395S |
|-----------------------------------------|-------|-------|
| Option                                  |       |       |
| DR $\mu$ -SW<br>CN                      |       | ○     |
| Option Drawwr<br>OPDCRN1<br>OPDCRN2, JS | x     | ○     |



### 13. OP-DISPLAY PWB PARTS LAYOUT



### 14. POP-UP DISPLAY PWB PARTS LAYOUT

