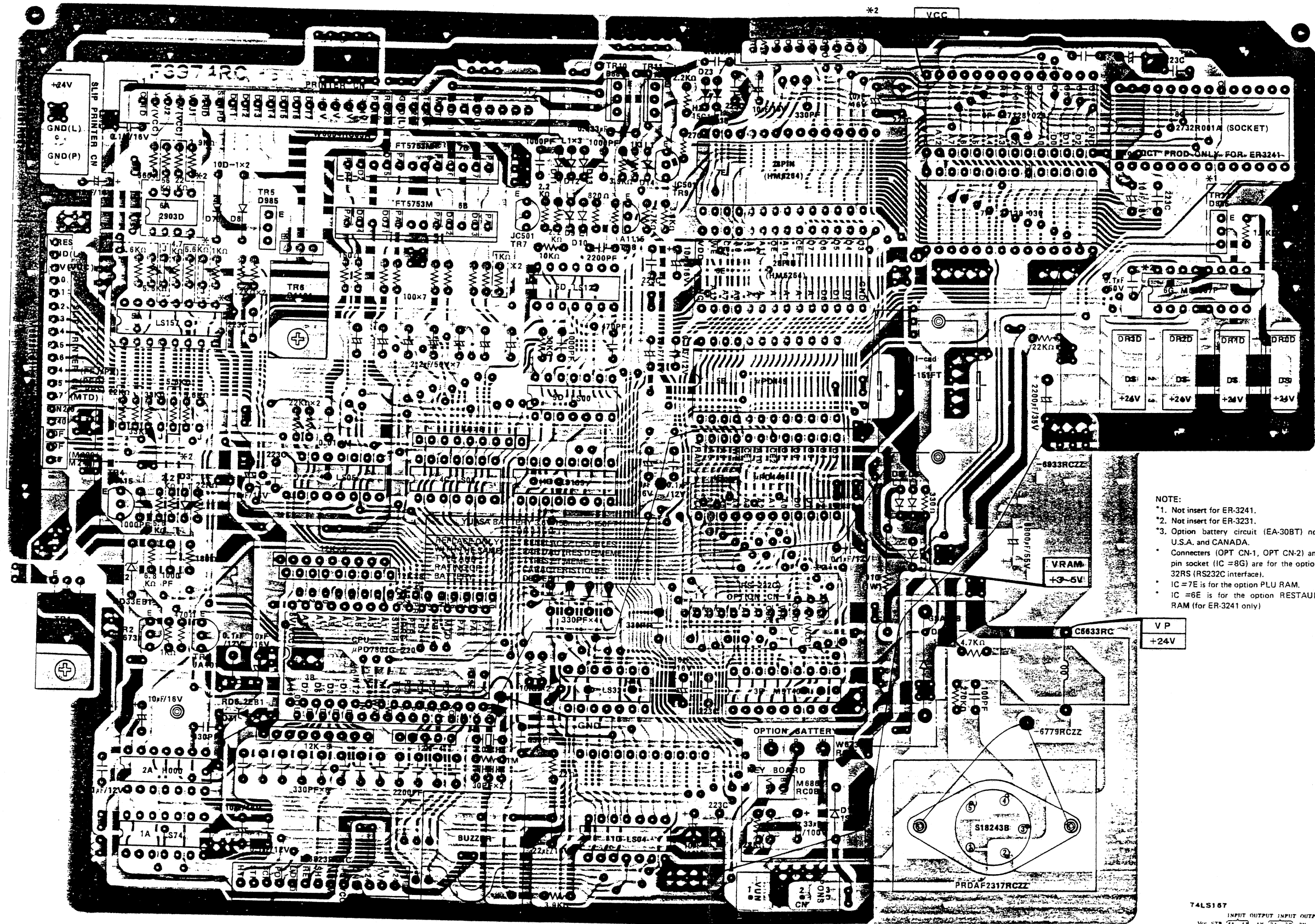
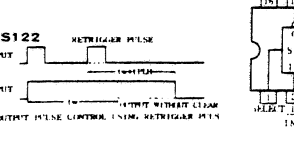
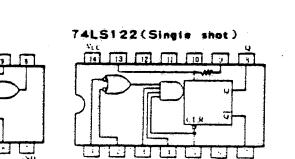
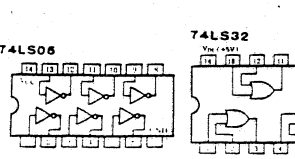
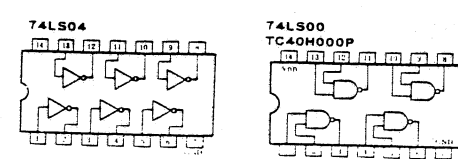
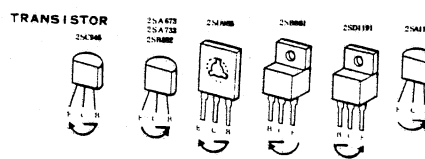
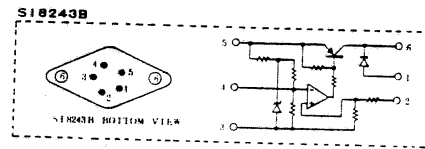




NOTE:
 *1. Not insert for ER-3241.
 *2. Not insert for ER-3231.
 *3. Option battery circuit (EA-30BT) not for U.S.A. and CANADA.
 • Connectors (OPT CN-1, OPT CN-2) and 24-pin socket (IC #8G) are for the option ER-32RS (RS232C interface).
 • IC #7E is for the option PLU RAM.
 • IC #6E is for the option RESTAURANT RAM (for ER-3241 only)



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FUNCTION TABLE

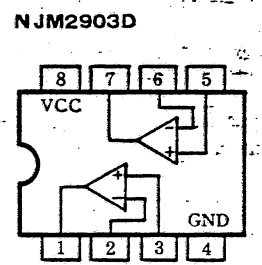
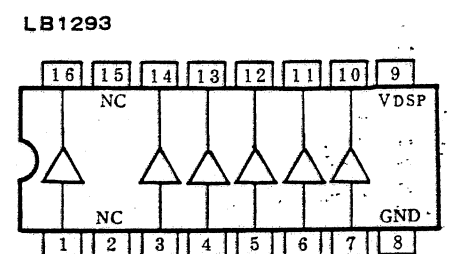
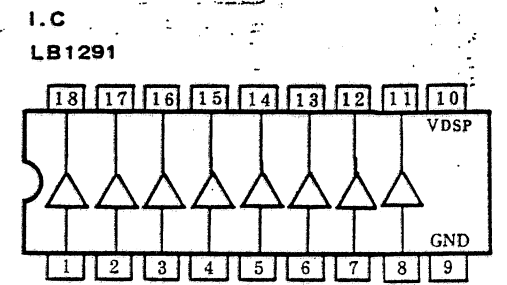
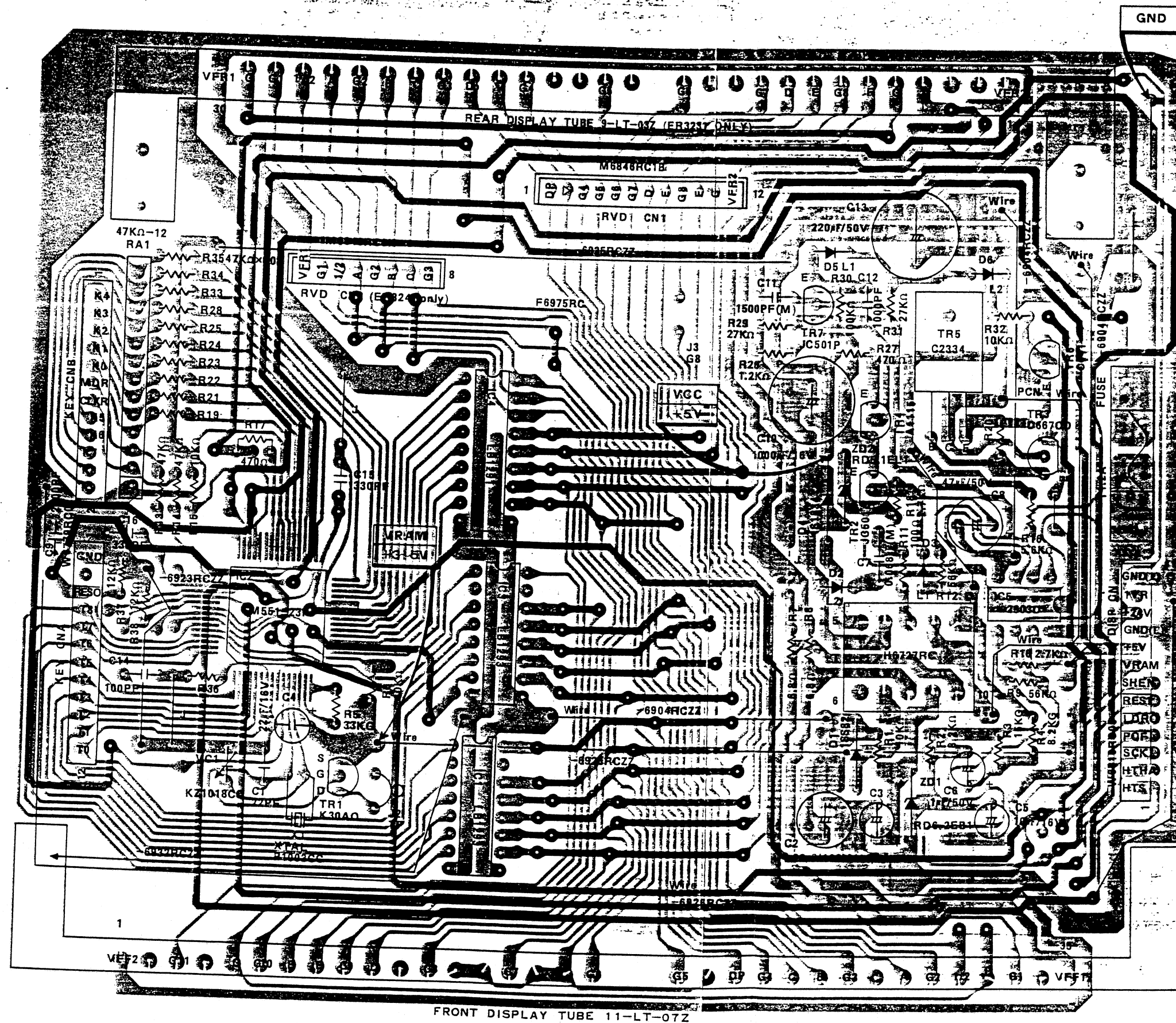
STROBE	SELECT	INPUTS			OUTPUT
G	S	A	B	Y	
H	X	X	X	L	
L	L	L	X	L	
L	L	L	L	H	
L	H	X	L	L	
L	H	X	H	H	

Positive logic

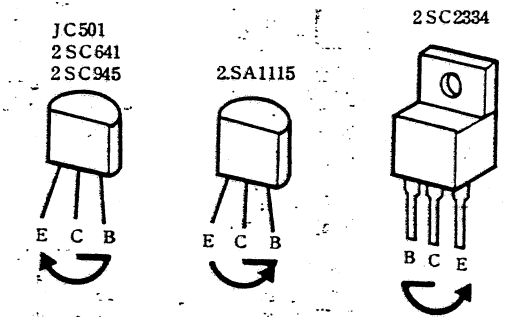
H = level at 1 outputs & inputs

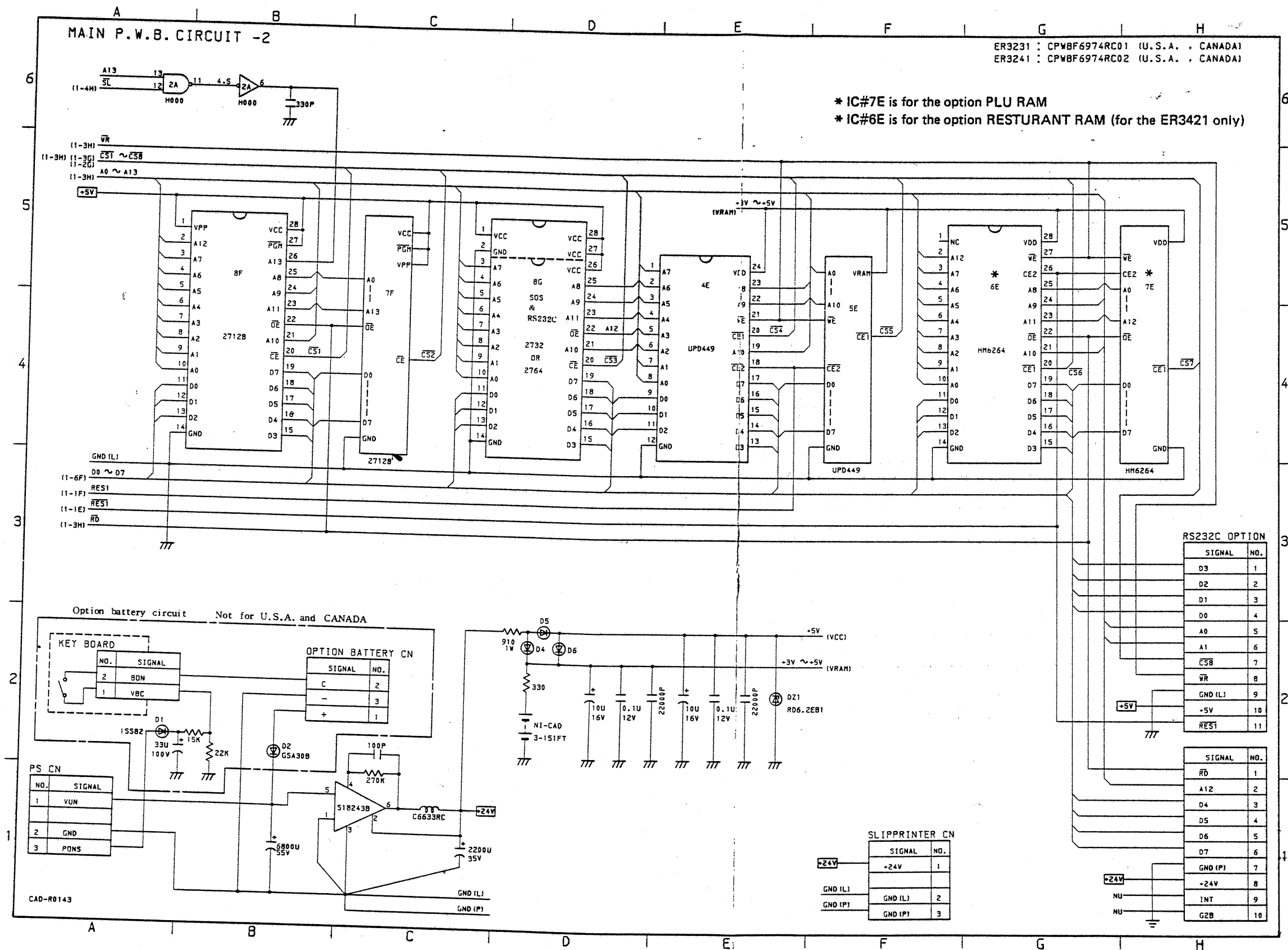
High level at 1 outputs & inputs

L = low level, 0 = low level, H = irrelevant



TRANSISTOR





MAIN BOARD LAYOUT (F6974RC) Please replace the MAIN P.W.B. LAYOUT of the ER3241/3231 with this.

